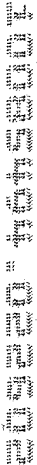
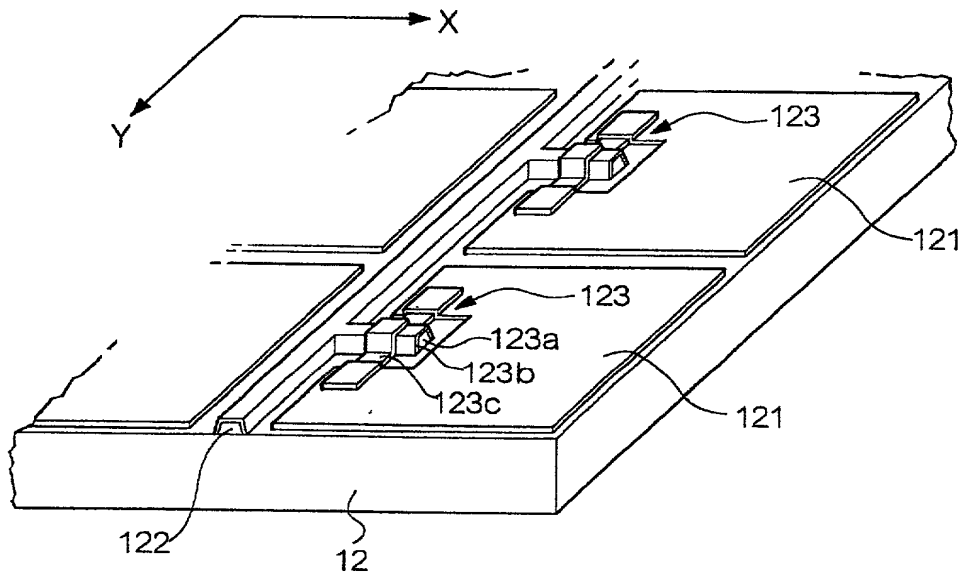


[illegible]



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FIG. 4

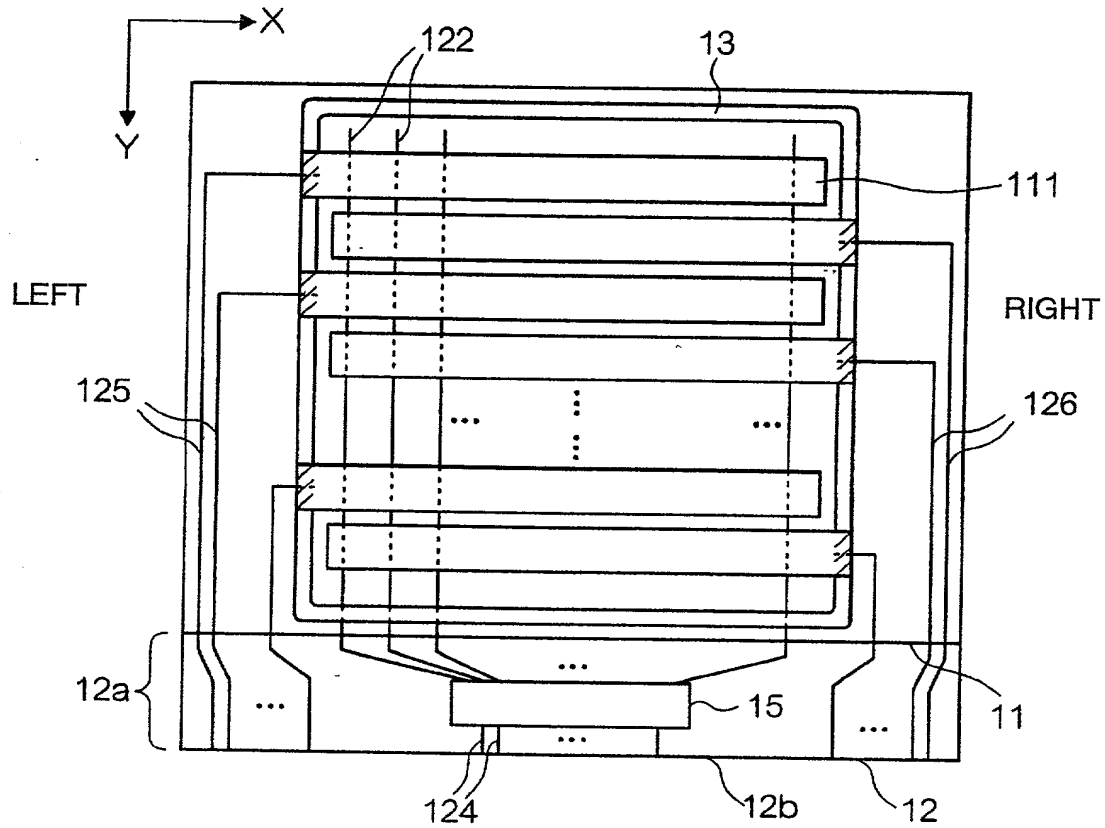


FIG. 5

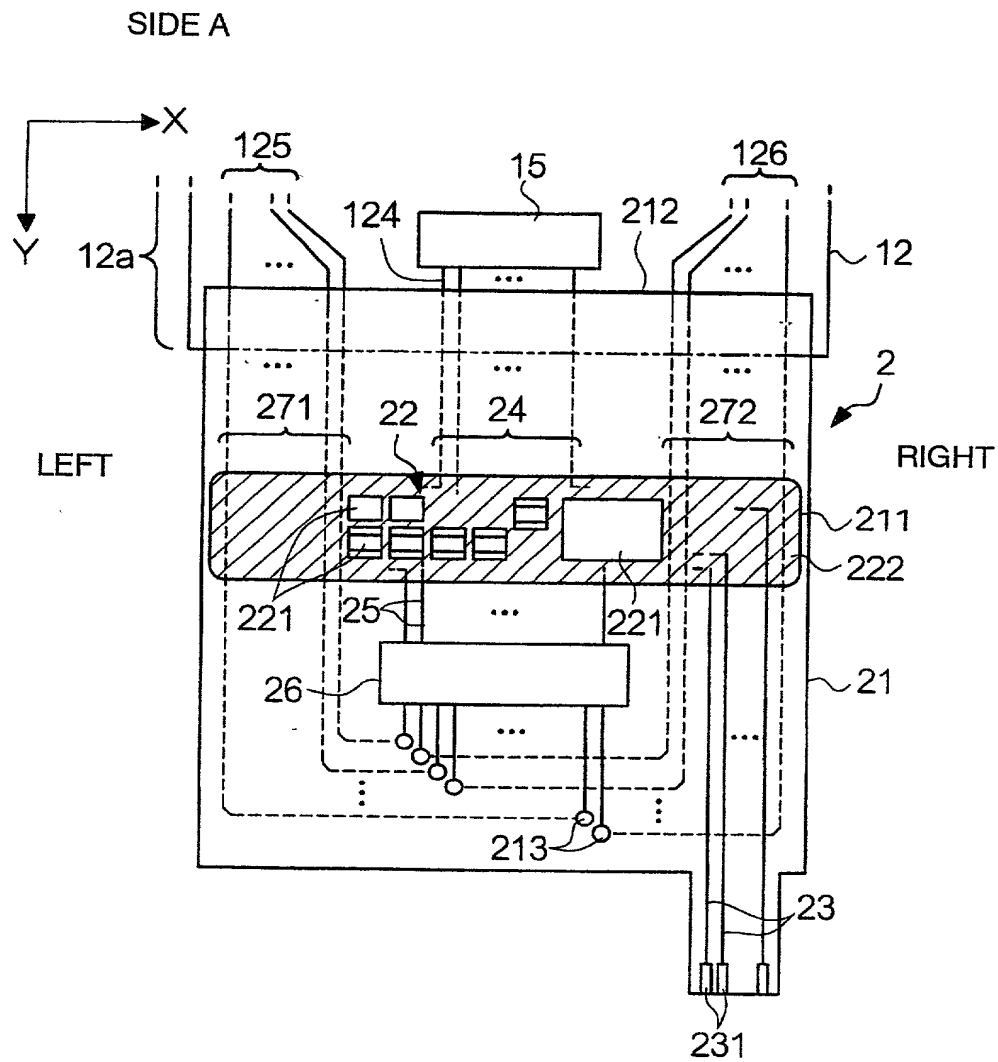


FIG. 6

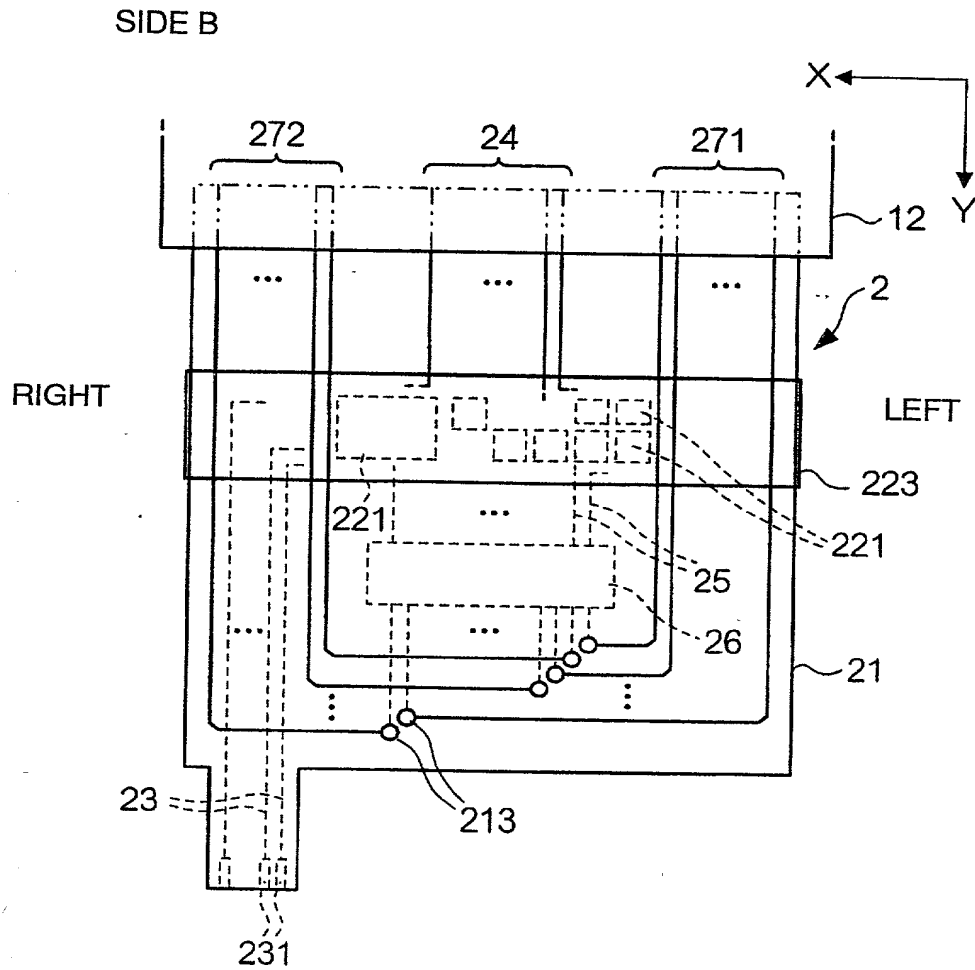
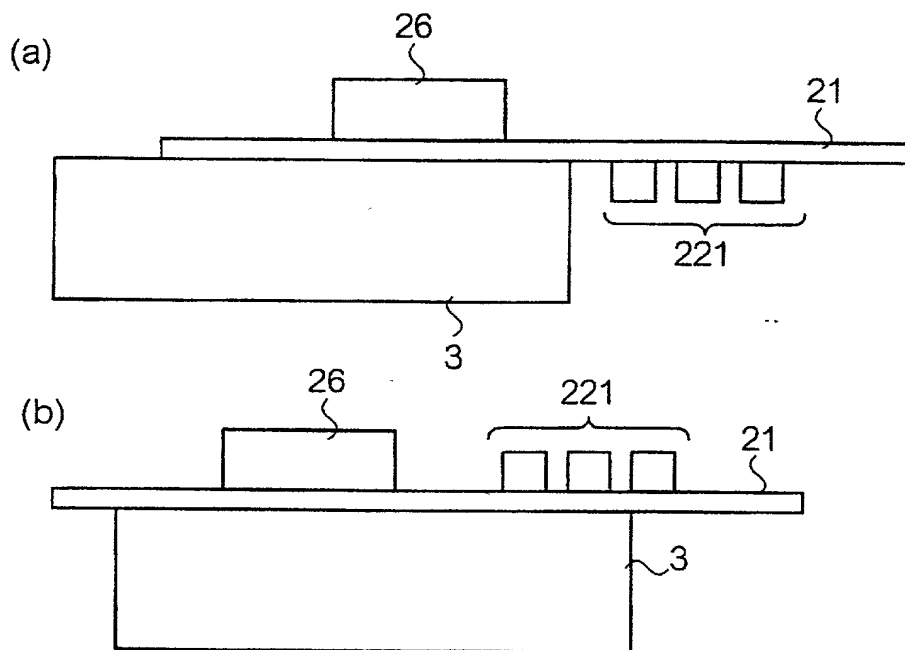


Figure 1 consists of two schematic diagrams, (a) and (b), illustrating a semiconductor device.

(a) Top view: This diagram shows a central rectangular region labeled 221. On either side of region 221 are two rectangular regions labeled 224. A dashed line, labeled 213, runs horizontally through the center of region 221. To the left of region 221, a horizontal line labeled 25 extends to the left, ending in an arrow labeled D. To the right of region 221, a horizontal line labeled 24 extends to the right, ending in an arrow labeled D'. A dashed line connects the right side of region 224 to the right side of region 221.

(b) Cross-sectional view: This diagram shows a cross-section of the device. A central vertical structure is labeled 221. On either side of this central structure are two rectangular regions labeled 222. Below these regions is a horizontal layer labeled 224. Below the layer 224 is another horizontal layer labeled 24. A vertical line, labeled 213, passes through the center of the device, extending from the top of region 221 down to the bottom of layer 24. A horizontal line labeled 25 is shown on the left side, and a horizontal line labeled 24 is shown on the right side.

FIG. 8



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FIG. 9

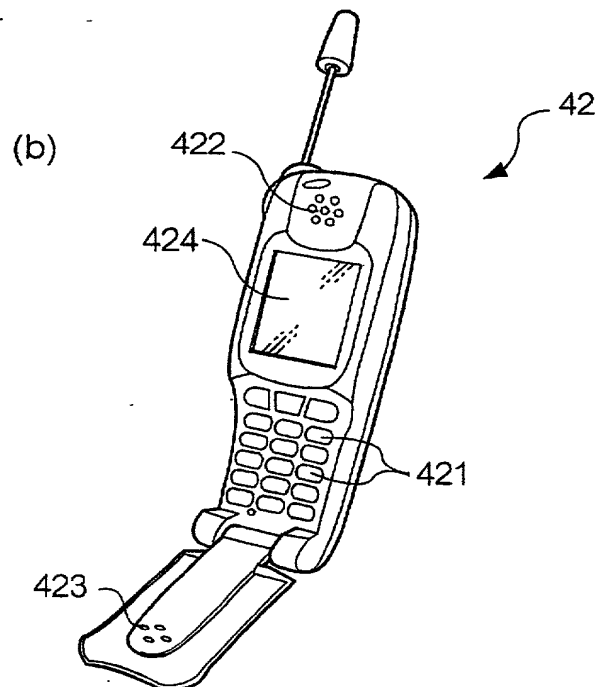
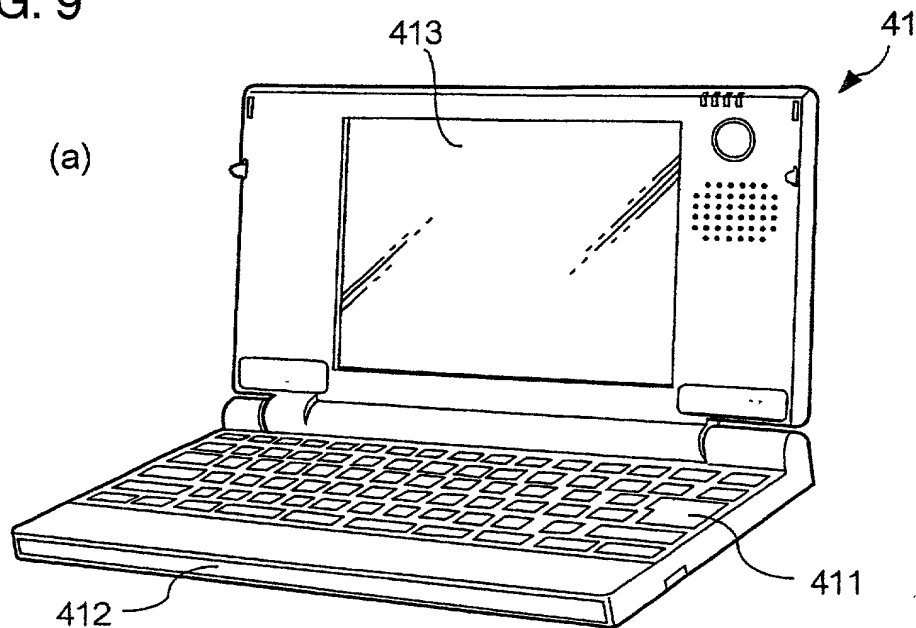




FIG. 10

